

ANTENNA VIOLATION CORRECTION IN HIGH-DENSITY INTEGRATED  
CIRCUITS

ABSTRACT OF THE DISCLOSURE

A method for correcting antenna violations in high-density integrated circuits (IC) begins by determining location of an antenna violation within a layout of a high-density integrated circuit. The processing continues by determining an affected input of a cell of the high-density integrated circuit based on the location of the antenna error. The processing then continues by identifying an available charge protection element. The processing further continues by logically coupling the available charge protection element to the affected input of the cell.